ABSTRACT

According to a method of designing an integrated circuit, a plurality of outgoing lines branch off from each of main lines of respective power supply paths on the sides of VDD and VSS, and the pitches between adjacent outgoing lines of the plurality of outgoing lines are set so as to be equal to each other. Preferably, branching positions of the plural outgoing lines of one power supply path correspond to branching positions of the plural outgoing lines of another power supply path in the longitudinal direction of the power supply paths, or the lengths of the plural outgoing lines of one power supply path are set so as to be longer than the lengths of the plural outgoing lines of another power supply path.

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